

Notice of References Cited	Application/Control No. 10/022,856	Applicant(s)/Patent Under Reexamination FECTEAU ET AL.	
	Examiner Qutub Ghulamali	Art Unit 2637	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,611,538	08-2003	Malerevich et al.	370/503
	B	US-6,128,337	10-2000	Schipper et al.	375/229
	C	US-2005/0069041	03-2005	Lincoln, Daniel J.	375/257
	D	US-6,128,347	10-2000	Nomura, Masahiro	375/257
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)				
	U	Mikazuki, T.; Matsui, N.;, "Statistical design techniques for high-speed circuit boards with correlated structure distributions", IEEE, Transaction on components, Packaging and Manufacturing technology, Vol. 14, Issue 3, Sept. 1991, pages 512-517□□				
	V	Mizuno et al, "Clock Distribution Networks with On-Chip Transmission Lines", IEEE International Conference, June 5-7, 2000, pages 3-5				
	W					
	X					

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.